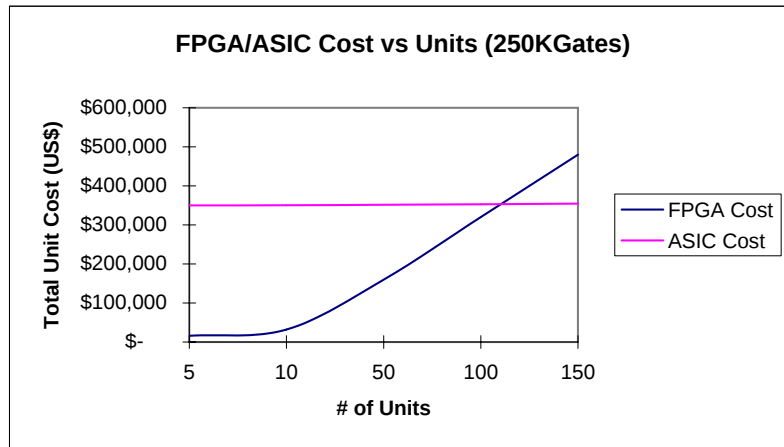


Device Only Cost (ASIC includes NRE)

Unit's	FPGA Cost	ASIC Cost
5	\$ 16,000	\$ 350,150
10	\$ 32,000	\$ 350,300
50	\$ 160,000	\$ 351,500
100	\$ 320,000	\$ 353,000
150	\$ 480,000	\$ 354,500

FPGA	\$	3,200	Each
FPGA NRE	\$	-	
ASIC	\$	30	Each
ASIC NRE	\$	350,000	

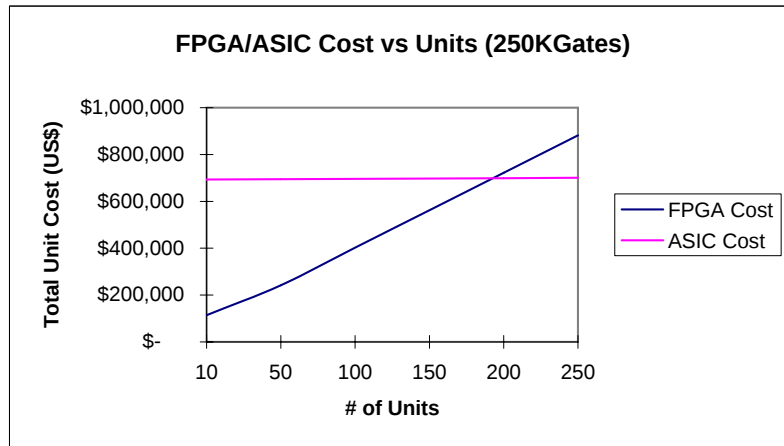


Device + EDA Tools Estimate (ASIC includes NRE)

FPGA EDA	\$	82,000	Simulation+Synthesis+FPGA Place&Route
ASIC EDA	\$	343,000	Simulation+Synthesis+Timing+ATPG

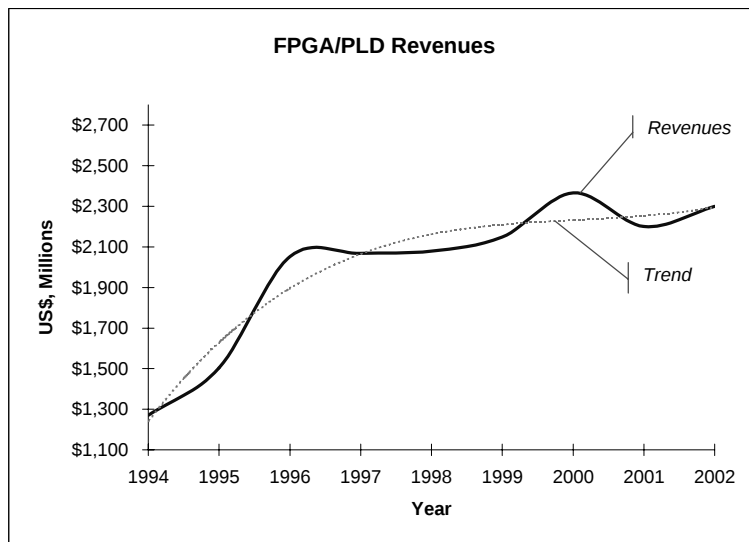
Unit's	FPGA Cost	ASIC Cost
10	\$ 114,000	\$ 693,300
50	\$ 242,000	\$ 694,500
100	\$ 402,000	\$ 696,000
150	\$ 562,000	\$ 697,500
200	\$ 722,000	\$ 699,000
250	\$ 882,000	\$ 700,500

FPGA	\$	3,200	Each
FPGA NRE	\$	-	
ASIC	\$	30	Each
ASIC NRE	\$	350,000	



**FPGA/PLD
Revenues (US\$,
Millions)**

1994	\$	1,270
1995	\$	1,504
1996	\$	2,052
1997	\$	2,068
1998	\$	2,079
1999	\$	2,149
2000	\$	2,367
2001	\$	2,200
2002	\$	2,300



**ASIC Revenues
(US\$, Millions)**

1994		
1995		
1996	\$	2,000
1997	\$	6,300
1998	\$	9,800
1999	\$	12,900
2000	\$	17,917
2001	\$	16,600
2002	\$	13,551

